

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4915	sram and (substrate\$1 same transistor\$1 same gate\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/14 09:46
L2	1840	sram and (substrate\$1 same transistor\$1 same gate\$1) and ((word same bit) near line\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/14 09:46
L3	2243	sram and (substrate\$1 same transistor\$1 same (gate\$1 with electrode\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/14 09:48
L4	830	sram and (substrate\$1 same transistor\$1 same (gate\$1 near electrode\$1)) and ((word same bit) near line\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/14 09:48
L5	28	sram and (substrate\$1 same (six with transistor\$1) same (gate\$1 with electrode\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/14 09:47
L6	16	sram and (substrate\$1 same (six with transistor\$1) same (gate\$1 near electrode\$1)) and ((word same bit) near line\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/14 09:47
L7	457	sram same (substrate\$1 same transistor\$1 same (gate\$1 with electrode\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/14 09:48
L8	189	sram same (substrate\$1 same transistor\$1 same (gate\$1 near electrode\$1)) and ((word same bit) near line\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/14 09:48
L11	99	(365/188.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/14 09:59
L12	15	1 and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/14 09:59

L13	12	2 and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/14 09:59
L14	6	3 and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/14 10:00
L15	4	4 and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/14 10:00
L16	1	7 and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/14 10:00
L17	0	8 and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/14 10:00